

#3 ② 8/21/98

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )

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NORMAN, and SANJAY MEHROTRA )

Serial No.: UNASSIGNED )

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For: FLASH EEPROM SYSTEM )

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San Francisco, California

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Washington, D.C. 20231

09103056-062290-9502160

TRANSMITTAL OF FORMAL DRAWINGS

Sir:

Please substitute the attached formal drawings, comprising five sheets, for the informal drawings presently of record.

Respectfully submitted,

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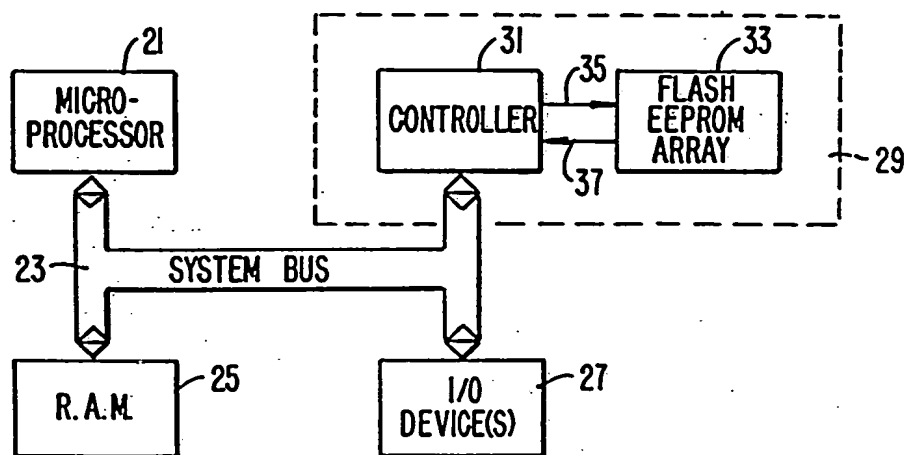


FIG. 1A.

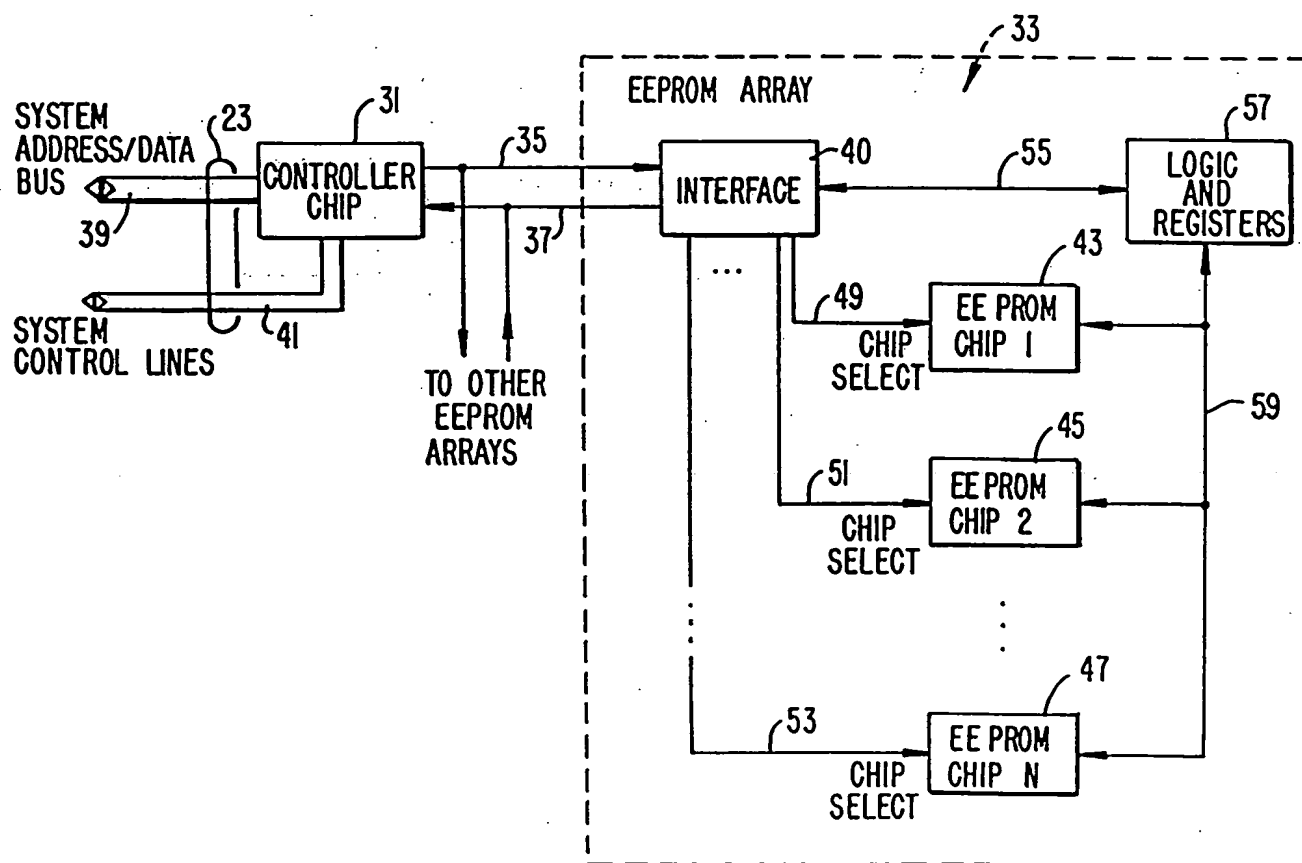
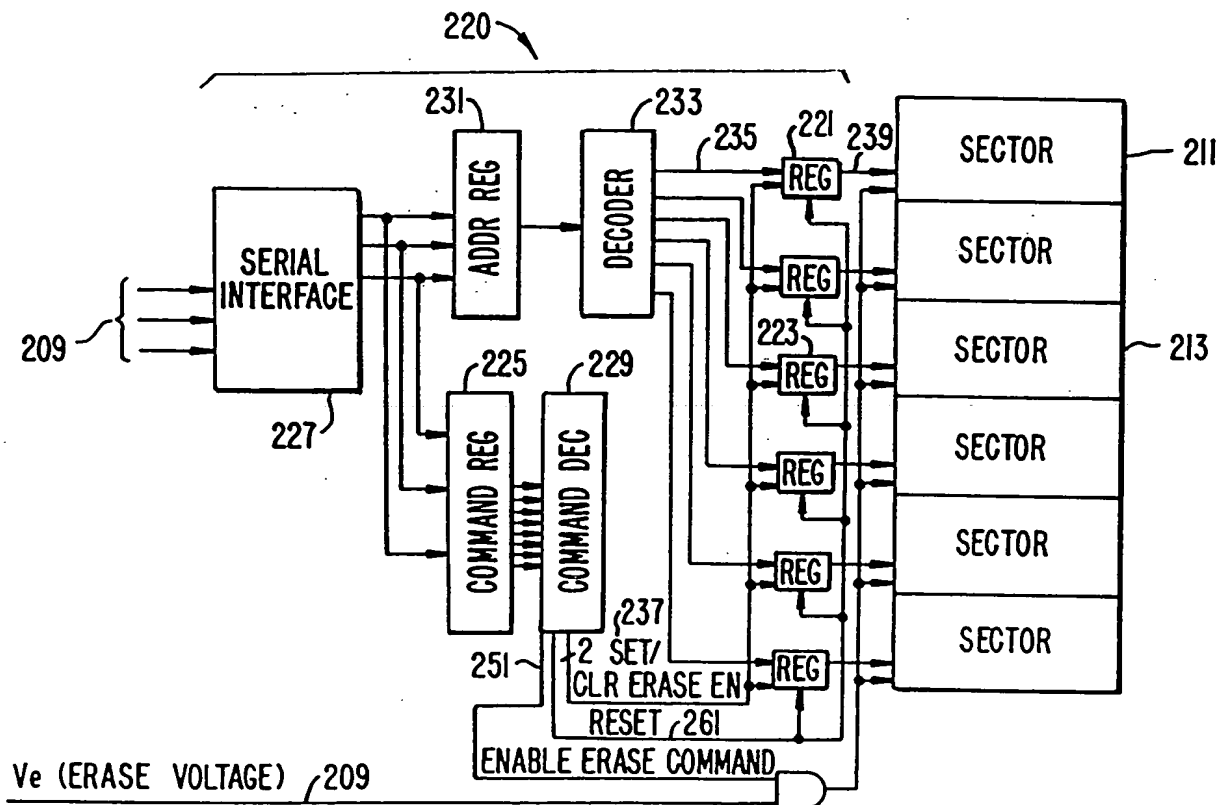
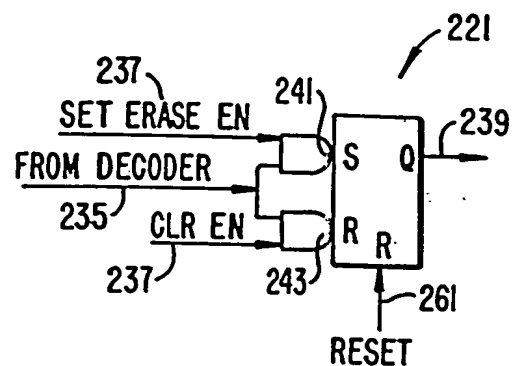
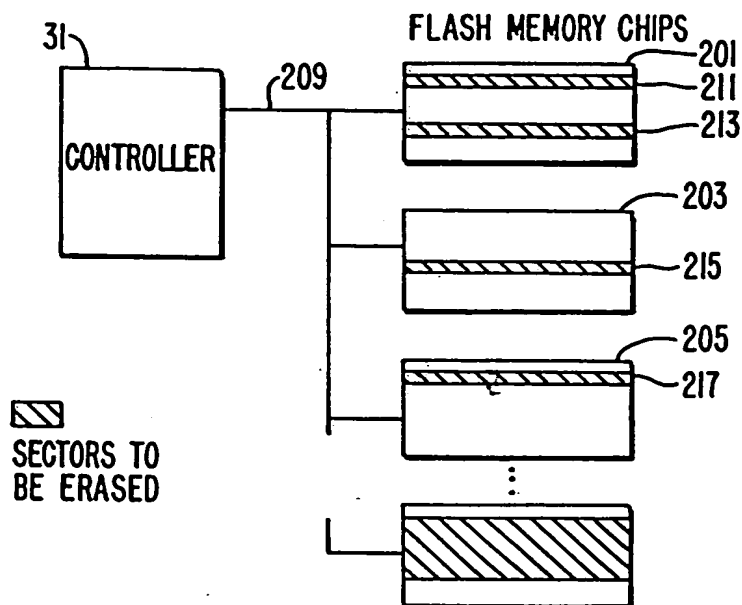


FIG. 1B.

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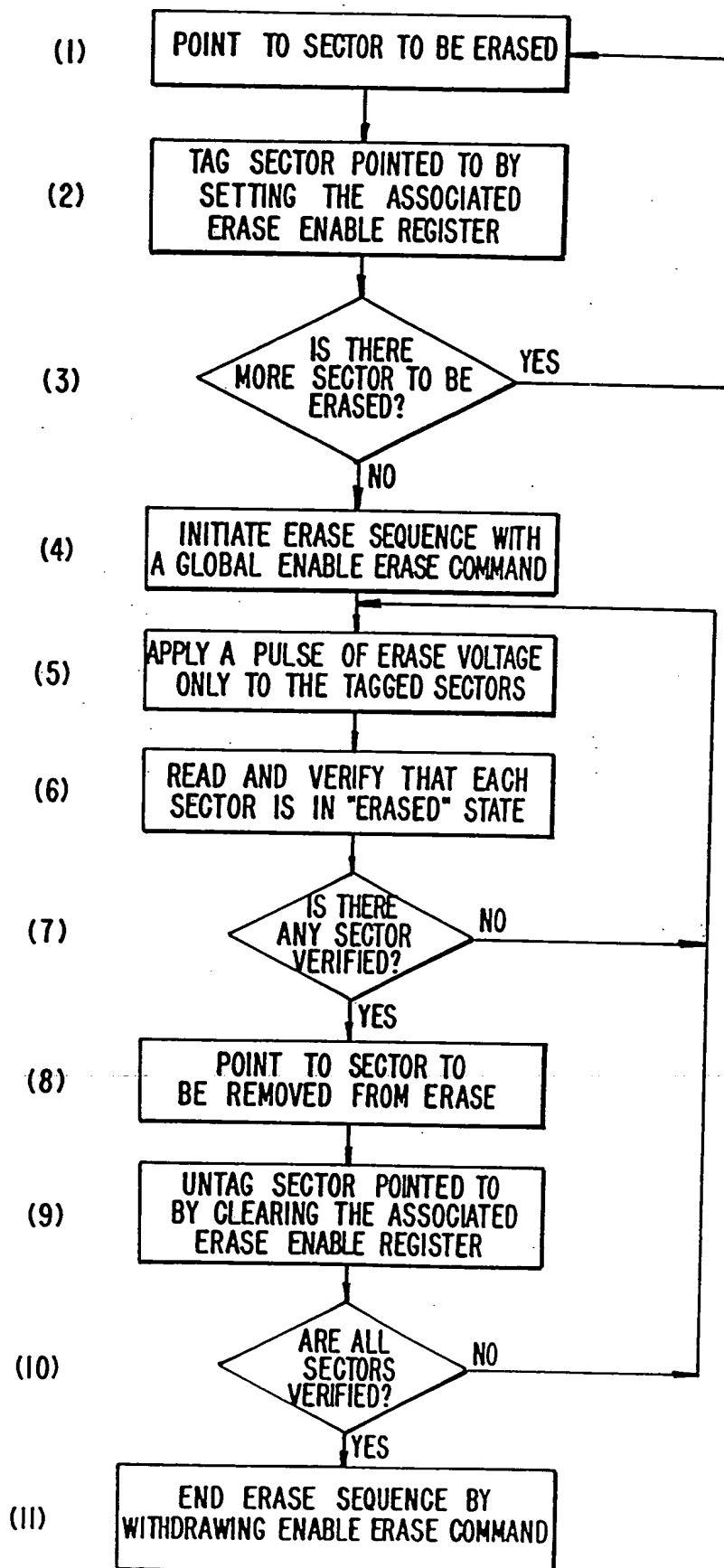
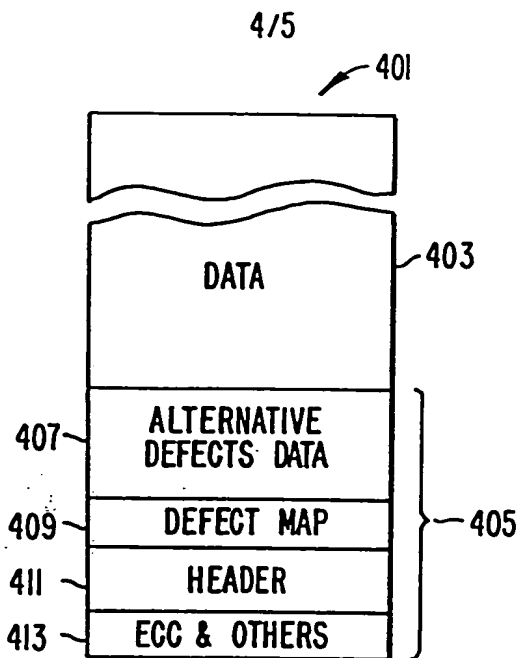


FIG.\_4.

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SECTOR PARTITION  
FIG. 5.

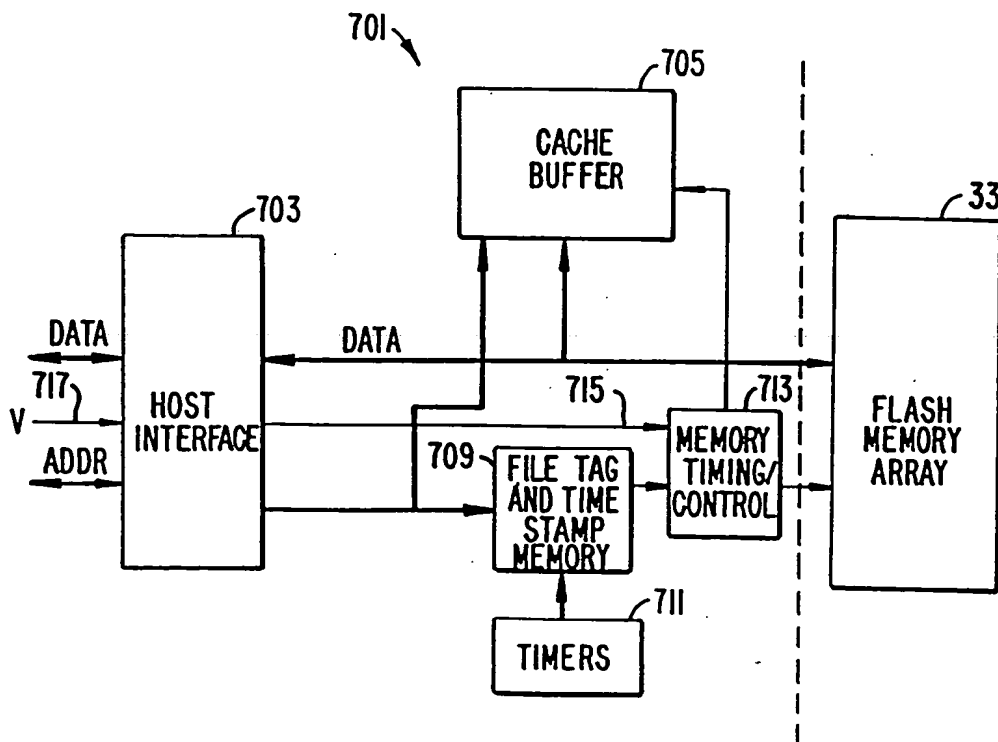
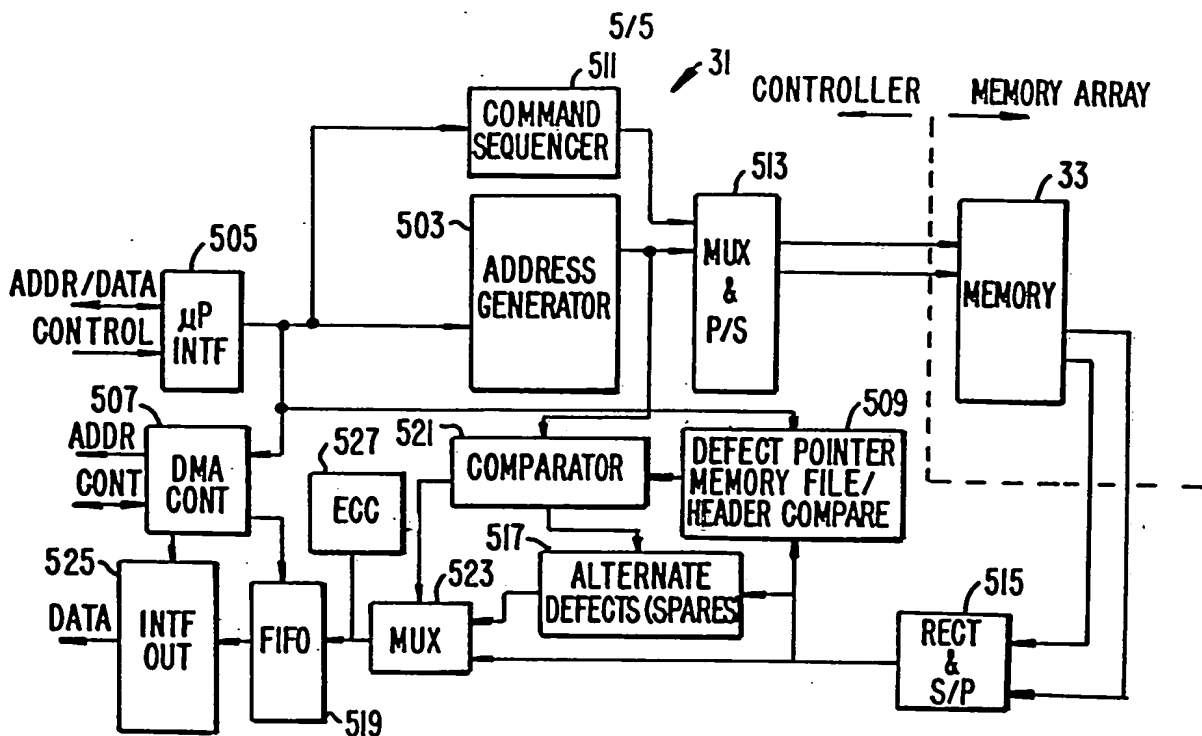


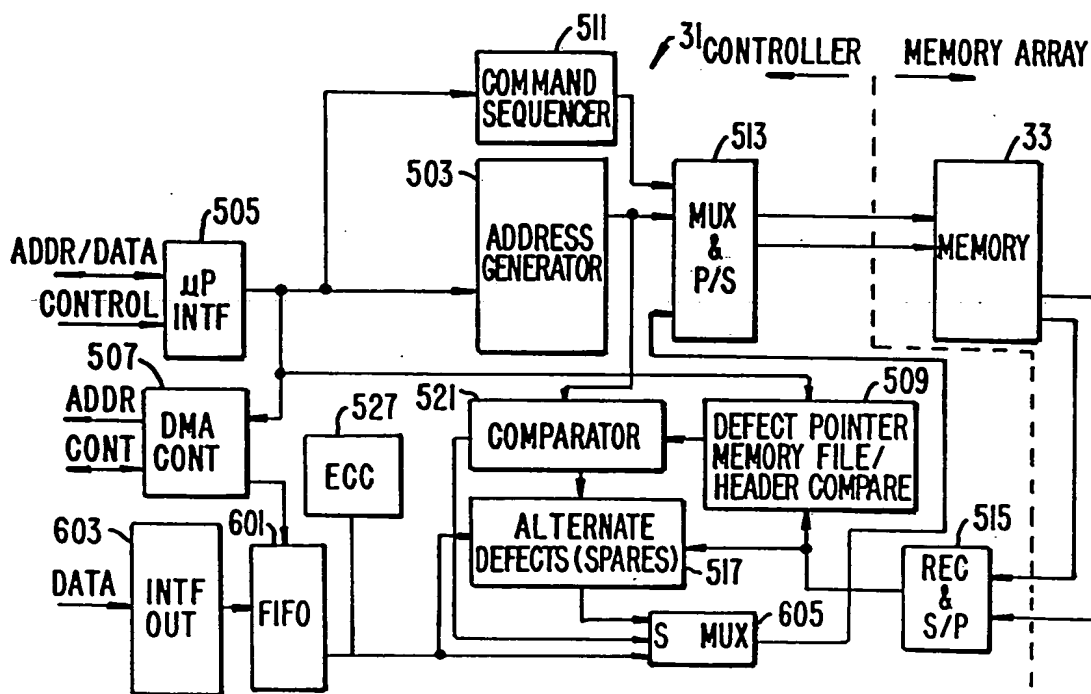
FIG. 8.

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READ DATA PATH CONTROL

FIG. 6.



WRITE DATA PATH CONTROL

FIG. 7.

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